



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

## \*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 7723

SERIAL NUMBER 10/620,286	FILING DATE 07/14/2003  RULE	CLASS 326	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. LATC-01049US0
-----------------------------	---------------------------------------	--------------	------------------------	--------------------------------------

## APPLICANTS

Om P. Agrawal, Los Altos, CA;

Bradley A. Sharpe-Geisler, San Jose, CA;  
Bai Nguyen, Union City, CA; Yu Huang, Cambridge, MA;  
Jack Wong, Fremont, CA;

\*\* CONTINUING DATA \*\*\*\*\* *all yes*

This application is a CIP of 10/406,050 04/02/2003  
which is a CIP of 10/194,771 07/12/2002

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *Re new*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/24/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>he</i> Initials <i>he</i>	CA	12	11	3

## ADDRESS

29416  
LATTICE SEMICONDUCTOR CORPORATION  
5555 NE MOORE COURT  
HILLSBORO , OR  
97124-6421

## TITLE

Field programmable gate array having partitionable embedded memory with configurable depth versus width

FILING FEE  RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	---